CORRIGENDUM No. 3, DATED 05.01.2024

FOR

TENDER NO. BNPM/NCB/366/2023-24 dated 10.11.2023

TENDER FOR UPGRADATION OF LAN (LOCAL AREA NETWORK) INFRASTRUCTURE AT BNPM PLANT, MYSURU

BNPM/NCB/366/2023-24	BANK NOTE PAPER MILL INDIA PRIVATE LIMITED **Tender for Upgradation of LAN (Local Area Network) Infrastructure at BNPM, Mysuru.** **CORRIGENDUM NO. 3**	SHEET 1 OF 1

1.0 SCOPE OF THIS CORRIGENDUM

1.1 This corrigendum dated 05.01.2024, is issued for,

(a) The tender due date extension for submission of tender published on 10.11.2023.

(b) Clarifications to queries received against the tender.

1.2 Except for details mentioned herein, all other details contained in the tender no. BNPM/NCB/366/2023-24 dated 10.11.2023, shall remain applicable and unchanged.

2.0 REVISION TO THE TENDER:-

2.1 Clause no. 1.1 (a) is as below

Closing date and time for receipt of tenders	**15-01-2024 - 1100 Hours**
Date and time of opening of Techno-commercial bid	**15-01-2024 - 1130 Hours**

3.0 CLARIFICATIONS/AMENDMENTS TO THE TENDER:-

3.1 Clause no 1.1 (b) is clarified/revised/amended as given below. Clarifications/revisions/ amendments made in Section VII – Technical Specifications will apply *mutatis mutandis* to Section VIII - Quality Control Requirements.

BNPM/NCB/366/2023-24	**BANK NOTE PAPER MILL INDIA PRIVATE LIMITED** **Tender for Upgradation of LAN (Local Area Network) Infrastructure at BNPM, Mysuru.** **CORRIGENDUM NO. 3**	SHEET 1 OF 1

S No	Tender Clause	Provision as per Corrigendum-1 dated 11.12.2023	Provision as per Corrigendum-2 dated 22.12.2023	Bidders Query (Request to change)	Revision/Amendment to tender terms & conditions
1.	*Section VII - Technical Specifications (Clause-15(a)(1): Core Switch – Switch Hardware):* 24 x 1/10G SFP+ ports, 12 x 1/10G Base-T ports, 4 x 40G QSFP+ ports with expansion capacity of 3 slots for future use to expand 1G, 10G, 40G & 100G for future scalability, Switch should work in non-blocking architecture at wire speed, redundant hot swappable power supplies. All types of SFP, stacking modules, Transceivers and relevant copper and Optical patch cords & power cables should be loaded as per functional drawing (as per proposed design / architecture).	*S No (3) of Corrigendum-1 (Pg. 8 of 14):* 24 x 1/10G SFP+ ports, 12 x 1/10G Base-T ports / **SFP+ copper module ports**, 4 x 40G/**100G QSFP28 ports** with expansion capacity of 3 slots for future use to expand 1G, 10G, 40G & 100G for future scalability, Switch should work in non-blocking architecture at wire speed, redundant hot swappable power supplies. All types of SFP, stacking modules, Transceivers and relevant copper and Optical patch cords & power cables should be loaded as per functional drawing (as per proposed design / architecture).	*S No 3.1 (2) of Corrigendum-2 (Pg. 3 of 3):* 24 x **10G** SFP+ ports, 12 x **10G** Base-T ports / SFP+ copper module ports, 4 x 40G/100G QSFP28 ports with expansion capacity of 3 slots for future use to expand 1G, 10G, 40G & 100G for future scalability, Switch should work in non-blocking architecture at wire speed, redundant hot swappable power supplies. All types of SFP, stacking modules, Transceivers and relevant copper and Optical patch cords & power cables should be loaded as per functional drawing (as per proposed design / architecture).	a. 24 x 10G SFP+ ports, 12 x 10G Base-T ports / SFP+ copper module ports, 4 x 40G/100G QSFP28 ports with expansion capacity of **1 slot** for future use to expand 1G, 10G, 40G & 100G for future scalability, Switch should work in non-blocking architecture at wire speed, redundant hot swappable power supplies. All types of SFP, stacking modules, Transceivers and relevant copper and Optical patch cords & power cables should be loaded as per functional drawing (as per proposed design / architecture). b. Need to remove expansion capacity of 3 slots for future use.	24 x 10G SFP+ ports, 12 x 10G Base-T ports / SFP+ copper module ports, 4 x 40G/100G QSFP28 ports, Switch should work in non-locking architecture at wire speed, redundant hot swappable power supplies. All types of SFP, stacking modules, Transceivers and relevant copper and Optical patch cords & power cables should be loaded as per functional Drawing (as per proposed design / architecture).